

computing@tanet.edu.te.ua www.tanet.edu.te.ua/computing ISSN 1727-6209 International Scientific Journal of Computing

INHERENT SIGNAL PREPROCESSING IN THE LINE CCD SENSOR

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Abstract: This paper is devoted to the description and practical verification of a new line CCD sensor control method. This method is used for inherent signal preprocessing or processing in line CCD sensor. This new method is suitable for movement compensation, one-frame filtration or two-frame filtration. Because of the similarity between a Non-Recursive Digital Filter with Finite Impulse Response (NRDF FIR) and the new mode we named it FIR mode. Conclusions and hints applicable for operation of line CCD sensor in FIR mode are also included in this paper.

Keywords: - CCD, Signal Processing, FIR Filter

1. INTRODUCTION

A FIR mode is one of the non-standard modes of a linear CCD sensor operation. We use this mode for inherent signal preprocessing in line CCD sensor. This mode is based on multiple exposures. A value of each output pixel is determined from values of N_F previous photo-elements, which means N_F exposures are needed to obtain an image. Each exposure has its own integration time, which has effect similar to the coefficients of filter impulse response.

2. THE FIR MODE

Output voltage in this mode is described by Eq. 1. Illuminance values of photo-elements $E_{n,j}$ are equal to samples of the image. Integration times T_{Xj} are equal to the coefficients of a FIR filter (impulse response). The entire frame integration time is equal to coefficient's sum. When we need to change the frame integration time all coefficients must by multiplied by ratio of old integration time and new integration time. Number of integration (exposures for one frame) N_F is equal to the filter length. The k_r and k_c are conversion constants and Q_0 representing parasitic charges generated in the CCD sensor.

$$u_{OUT}(i) = k_r \cdot k_C \cdot \sum_{j=0}^{N_F - 1} E_{n-j} \cdot T_{X(j+1)} + N_F \cdot Q_0 \qquad (1)$$

Fig. 1 shows a principle scheme of the line CCD sensor.

Sensor's signals and parts that we use in next text are shown in this figure.

3. PRINCIPLE OF FIR MODE OF OPERATION

FIR mode of operation can be build in simple control unit without complex arithmetic unit. All control functions are included in one simple subroutine. The subroutine flowchart is shown in Fig. 3. This subroutine



Fig. 1 – The line CCD sensor principle scheme.

corresponds to a cycle, which must be repeated N_F times in order to obtain one frame. Here is described the Line CCD sensor control in FIR mode.

The sensor must be cleared at the beginning of each frame by electronic shutter (signal Φ_S). At the same time the first coefficient is written to the timer control register. In this case a timer is used for integration time generation. The control cycle is described below.

The charges are transported by control signal Φ_X to the charge shift register after a lapse of integration time (timer interrupt occurred). The transported charges are added to the charges stored there from the last cycle. If the last cycle (exposure) is executed the read-out is processed (the frame is complete) otherwise the charges in shift register are moved by

one position by means of control signal Φ_{l} .



Fig. 2 – Flowchart of the FIR mode control algorithm.

The next coefficient is loaded to timer control register after the charge is moved. If the coefficient is equal to zero only next charge move is processed and the next coefficient is loaded. The next exposure begins after the timer is loaded.

4. EXAMPLES OF FIR MODE APPLICATIONS

There are many applications for the FIR mode of operation. Only two of them are discussed here, movement compensation and one-frame filtration (especially using one-frame filtration for data decimation).

Fig. 2 shows a block scheme of the line CCD camera we used for experimental verification of this mode.



Fig. 3 – The line CCD camera principle scheme.

5. MOVEMENT COMPENSATION

An image of a moving object obtained with a long integration time is needed in some cases. It is impossible to obtain a sharp image using standard mode with long integration time because the object must not significantly change its position during the integration time. The measurement set-up for this reason is shown in fig. 4.



Fig. 4 – Measurement of moving object set-up.

The FIR mode of operation can be used even in this case. All FIR filter coefficients must have the same length in this case. The charge in the shift register has a velocity determined by coefficients length. The sharp image is obtained (coherence integration occurs) when the charge velocity in the shift register is equal to the velocity of the image on the CCD sensor. The frame has N_F times higher illumination when it is obtained in FIR mode with N_F coefficients then sharp frame obtained in standard mode. The optimum number of the coefficients depends on measurement conditions and may be calculated from eq. 2. The optimum integration time per one coefficient may be calculated as $t_{INT} = T_X/N$.

$$N = (v_{IM} \cdot T_X) \operatorname{div} (d_{PIX}) = (v_O \cdot T_X) \operatorname{div} (M \cdot d_{PIX})$$
(2)

where T_X is required integration time and next conotation of the variables follows from the fig. 3.

As an example are shown moving test tape image obtained using standard mode (in fig. 6) and image obtained by FIR mode with 32 coefficients (in fig. 7). Both images had same integration time T_X equal to 9.344 ms. Test tape velocity v_0 was 0.44 m/s. Charges velocity in charge shift register v_{CH} was 0.048 m/s. Lens magnification was M=0,11. We can compare these images with reference static image obtained by standard mode (in fig. 5) obtained with the same conditions.



Fig.5 – Test tape static image obtained in Standard mode, The average edge width is 3 pixels.



Fig.6 – Moving test tape image obtained in Standard mode. The average edge width is 23 pixels.



Fig.7 – Moving test tape image obtained in FIR mode. The average edge width is 5 pixels.

We can see that the image obtained in FIR mode has comparable widths of edges as the reference image. But image obtained in the standard mode has away wider edges.

6. ONE-FRAME FILTRATION

The one-frame filtration means that the FIR filter is calculated during obtaining one frame. We have a method called two-frame filtration where the complete filtration is done by subtracting two frames obtained with different coefficients. The one-frame filtration is calculated all in the line CCD sensor.

We used the one-frame filtration to build a low pass antialiasing filter for decimation. Decimation is correct way to decrease number of signal's samples by factor K. The relative frequencies higher then 1/Kmust be removed from the signal before resampling by factor K to avoid aliasing. For these purposes a low pass filter with relative stop frequency 1/K is used. Fig. 8 shows three ways of decimation realisation. Fig. 8a) shows decimation after A/D conversion, all video-signal pixles are converted. A digital low pas filter (DLP) is used. Fig. 8b) shows decimation before A/D conversion, there is converted to digital only decimation number of pixels. An analogue LP filter (ALP) is used. Fig. 8c) shows the decimation we used. The LP filter is realised in the CCD sensor using FIR mode of operation and resampling is done before conversion.

This is a simple way to realise decimation. It is applicable for simple measuring systems such as a photoelectric gauge (sensor for measurement of dimensions).



Fig. 8 – Decimation examples a) digital decimation scheme, b) decimation before A/D conversion, c) decimation scheme we used.

There is an example where the only resampling and correct decimation is used to decrease number of video-signal pixels. In this example we realised decimation with decimation factor *K* equal to 8. The low pass filter with relative stop frequency on 1/K =0,125 was designed for this decimation. The filter transfer function $|H(j\Theta)|$ of designed filter is shown in fig. 9.



Impulse response $h_n(i)$ of the filter is shown in fig. 10. Values of coefficients of the impulse response answer to FIR filter coefficients. The filter was implemented in CCD sensor as LP decimation filter.

A test with increasing spatial frequency was used for decimation verification. The full-scale test image is shown in fig. 11. This waveform is acquired by standard method of control CCD sensor.



Connection between relative frequencies of waveform (circles) and amplitude of the frequencies (line) from fig. 11 is shown in fig. 12. Both courses are shown in dependency to signal position.



Fig. 11 – Image of the test obtained in normal mode.



Fig. 12 – Representation of frequencies (circle) and its amplitude (line) in waveform from fig. 11.

Waveform of resampled signal from fig. 11 is shown in fig. 12. An aliasing occurred in this image.



Fig. 13 – Test image resampled by factor K=8.

Connection between relative frequencies of waveform (circles) and amplitude of the frequencies (line) from fig. 13 is shown in fig. 14.

There you can see sliasing frequencies from 100th pixel to 175th-pixel in fig. 14 (fig. 13).

The test image obtained by FIR mode (LP filter implemented) is shown in fig. 15.

In fig. 16 is shown waveform of resampling signal from fig. 15. It is corresponds to correct decimation. There is no aliasing occurred.

In fig. 18 is shown the same situation as in fig. 14 for waveform acquired by FIR mode with LP filter and resampled with factor K = 8.

The ratio of power spectral density original signal to resampled signal is shown in fig. 18 resp. fig. 19. Here we can see that the aliasing occurs when only resampling is used. As the consequence of aliasing is occurrence of the false frequencies in image.



Fig. 14 – Representation of frequencies (circle) and its amplitude (line) in waveform from fig. 13.



Fig. 15 – Waveform of test image obtained in FIR mode (LP antialiasing filter is implemented).



Fig. 16 – The resampled waveform from fig. 15 resampled by factor K = 8.



Fig. 17 – Representation of frequencies (circle) and its amplitude (line) in waveform from fig. 16.

But when the correct decimation with LP filter is used the aliasing does not occur.

This method is applicable for decreasing the pixel number in image. The method does not require complex control circuits. For example one chip 8 bit microcontrollers like AT89xx or AT90Sxx are applicable.



Fig. 18 – Power spectral density ratio of waveform acquired by standard mode to resampled waveform by factor K = 8.



Fig. 19 – Power spectral density ratio of waveform acquired by FIR mode to resampled the waveform by factor K = 8.

7. CONCLUSIONS

The FIR mode of line CCD sensor operation is usable for inherent signal pre-processing in line CCD sensor. It can by use for movement object scanning too. There are some advantages:

- the complex circuit for FIR filter calculation is not needed only the time interval control is necessary

- the calculation is processed without amplitude quantification

- the FIR filter calculation is executed during time of integration

But there is a problem with designing of positive impulse response FIR filter. This problem can be eliminated by two-frame filtration.

8. ACKNOWLEDGEMENT

This work has been supported by the research project MSM 210000015 "Research of New Methods for Physical Quantities Measurement and Their Application in Instrumentation".

9. References

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